



08/479211

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Docket No. 0756-1363  
Date: June 7, 1995

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is a Rule 53(d) continuation-in-part patent application of:

Inventor(s):

*100% ET AL*

Hisashi OHTANI

Akiharu MIYANAGA

Hongyong ZHANG

Naoaki YAMAGUCHI

For: **METHOD FOR MANUFACTURING  
SEMICONDUCTOR DEVICE**

Enclosed are:

34 pages of specification, claims and abstract  
 34 sheets of drawings (informal) (Figs. 1A-4F)  
 An assignment of the invention to \_\_\_\_\_

Declaration and Power of Attorney.  
 Priority is claimed based on foreign application(s)  
No(s): 6-51237  
Filed on: February 23, 1994  
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Certified copy(s)  attached;  filed on  
February 21, 1995 in prior U.S.S.N. 08/391,580  
filed February 21, 1995  
 A verified statement to establish small entity status under 37 CFR  
1.9 and 37 CFR 1.27.

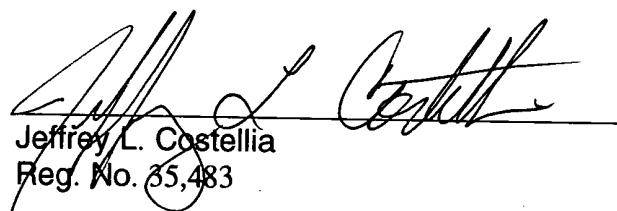
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Other: Preliminary Amendment, Sheet of Filing Particulars

A check in the amount of \$ \_\_\_\_\_ to cover the filing fee is  
enclosed.

No filing fee is enclosed.

Respectfully submitted,



Jeffrey L. Costellia  
Reg. No. 35,483

Sixbey, Friedman, Leedom & Ferguson, P.C.  
2010 Corporate Ridge, Suite 600  
McLean, Virginia 22102  
(703) 790-9110



## SHEET OF FILING PARTICULARS

Name and Address of Inventors

5 Hisashi OHTANI  
Mac Aikoishida Court  
501 7-896-1, Takamori, Isehara-shi  
Kanagawa-ken 259-11 JAPAN

10 Akiharu MIYANAGA  
2-505, 3-4-1, Minamigaoka  
Hadano-shi  
Kanagawa-ken, 257 JAPAN

15 Hongyong ZHANG  
Paresu Miyagami 302  
1-10-15, Fukamidai, Yamato-shi  
Kanagawa-ken, 242 JAPAN

15 Naoaki YAMAGUCHI  
2737, Shinohara-cho  
Kohhoku-ku, Yokohama-shi  
Kanagawa-ken, 222 JAPAN

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Title: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

SIXBEY, FRIEDMAN, LEEDOM & FERGUSON, P.C.  
2010 CORPORATE RIDGE, SUITE 600  
MCLEAN, VIRGINIA 22102

25 (703) 790-9110